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Jayant Baliga is an internationally recognized expert on power semiconductor devices. He is a Member of the National Academy of Engineering and a Fellow of the IEEE. He spent 15 years at the General Electric Research and Development Center, Schenectady, NY, leading their power device effort and was bestowed the highest scientific rank of Coolidge Fellow.

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Fundamentals of Power Semiconductor Devices will be of interest to practicing engineers in the power semiconductor device community and can also serve as an ideal textbook for teaching courses on power semiconductor devices due to the extensive analytical treatment provided for all device structures.

Fundamentals of Power Semiconductor Devices: Baliga, B ...
One person ready to experiment—in the literal sense—was B. Jayant Baliga, the recipient of this year's IEEE Medal of Honor. At the time, he was developing semiconductor power devices for General Electric, which used countless electric motors in its many products—countless motors that drew countless watts.

How B. Jayant Baliga Transformed Power Semiconductors
Prof. Baliga is an internationally recognized expert on power semiconductor devices. He is a Member of the National Academy of Engineering and a Fellow of the IEEE. He spent 15 years at the General Electric Research and Development Center, Schenectady, NY, leading their power device effort and was bestowed the highest scientific rank of Coolidge Fellow.

Jay Baliga • Electrical and Computer Engineering
Bantval Jayant Baliga is an Indian electrical engineer best known for his work in power semiconductor devices, and particularly the invention of the insulated gate bipolar transistor. Dr. B. Jayant Baliga wrote: "Power semiconductor devices are recognized as a key component of all power electronic systems. It is estimated that at least 50 percent of the electricity used in the world is controlled by power devices. With the wide spread use of electronics in the consumer, industrial, medical, and

B. Jayant Baliga - Wikipedia
B. JAYANT BALIGA, FELLOW, IEEE A bstract-Power devices based upon silicon technology are rapidly approaching their theoretical limits of performance. Consequently, it will be necessary to develop devices from other materials in the future in order to reduce power losses in bigb-frequency systems and in order to acbeive high efficiencies.

Power semiconductor device figure of merit for high ...
Trends in Power Semiconductor Devices B. Jayant Baliga, Fellow, IEEE (Invited Paper) Abstract-This paper reviews recent trends in power semicon- ductor device technology that are leading to improvements in power losses for power electronic systems.

Trends in Power Semiconductor Devices - Electron Devices ...
B. JAYANT BALIGA, Ph.D., FIEEE, NAE Member Distinguished University Professor of Electrical Engineering ... "Role of Power Semiconductor Devices in Creating a Sustainable Society", Invited Plenary Paper, IEEE Applied Power Electronics Conference (APEC), Long Beach, CA, March 18, 2013 – attendance of 3800 ...

B. JAYANT BALIGA, Ph.D., FIEEE, NAE Member
Although many universities are offering courses at the graduate level on power devices, the lack of a suitable textbook on this topic has been a hinderance. Two earlier books, Semiconductor Power Devices by S.K. Ghandhi and Modern Power Devices by B.J. Baliga, are out of print.

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Power semiconductor device figure of merit for high-frequency applications Abstract: A figure of merit (the Baliga high-frequency figure of merit) is derived for power semiconductor devices operating in high-frequency circuits.

Power semiconductor device figure of merit for high ...
Multiple types of power semiconductor amplifier device exist, such as the bipolar junction transistor, the vertical MOS field effect transistor, and others. Power levels for individual amplifier devices range up to hundreds of watts, and frequency limits range up to the lower microwave bands.

Power semiconductor device - Wikipedia
B. Jayant Baliga This textbook provides an in-depth treatment of the physics of power semiconductor devices that are commonly used by the power electronics industry.

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Jayant Baliga is an internationally recognized expert on power semiconductor devices. He is a Member of the National Academy of Engineering and a Fellow of the IEEE.

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From 1974 to 1988 Baliga was a member of the General Electric Corporate Research and Development Center, Schnectady, NY, where he was Manager of the High Voltage Device and IC programs. At GE he originated the concept of functional integration of MOS and bipolar physics for power devices.

B. Jayant Baliga - Engineering and Technology History Wiki
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FUNDAMENTALS OF POWER SEMICONDUCTOR DEVICES BY B.JAYANT ...
Wide Bandgap Semiconductor Power Devices: Materials, Physics, Design and Applications provides readers with a single resource on why these devices are superior to existing silicon devices. The book lays the groundwork for an understanding of an array of applications and anticipated benefits in energy savings.

Wide Bandgap Semiconductor Power Devices - 1st Edition
ionization coefficients in semiconductors has been added. In Chap. 3 on "Breakdown Voltage," the analytical model has been improved to match experimental results for silicon devices by using Baliga's power law for the